

FIG. 1

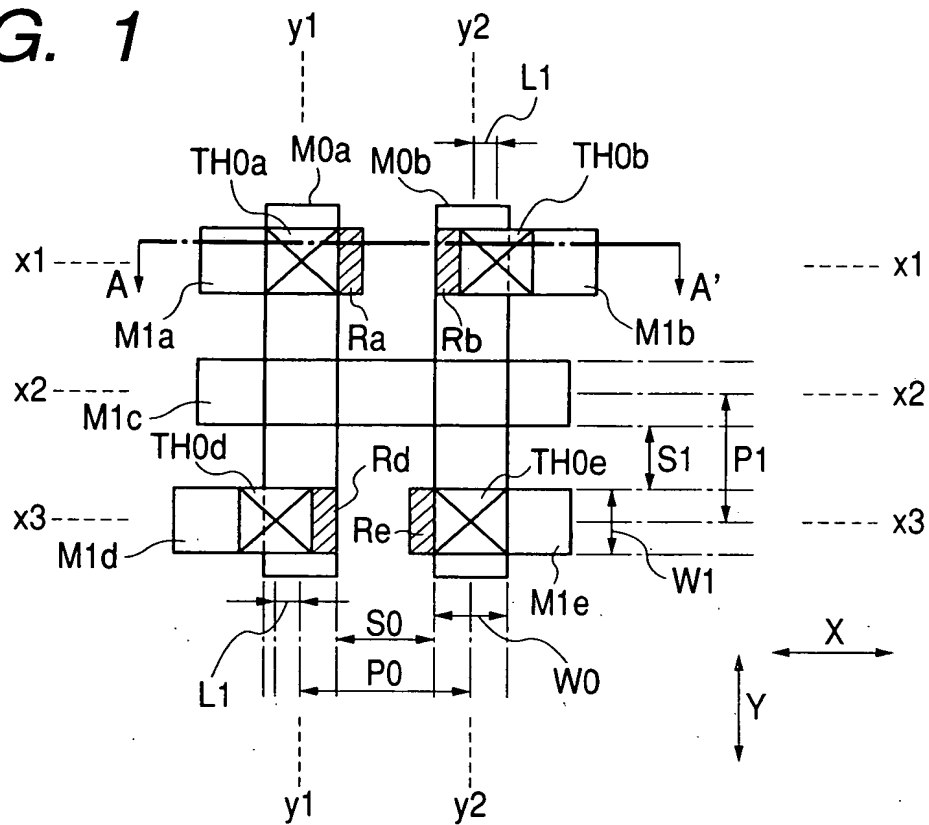


FIG. 2

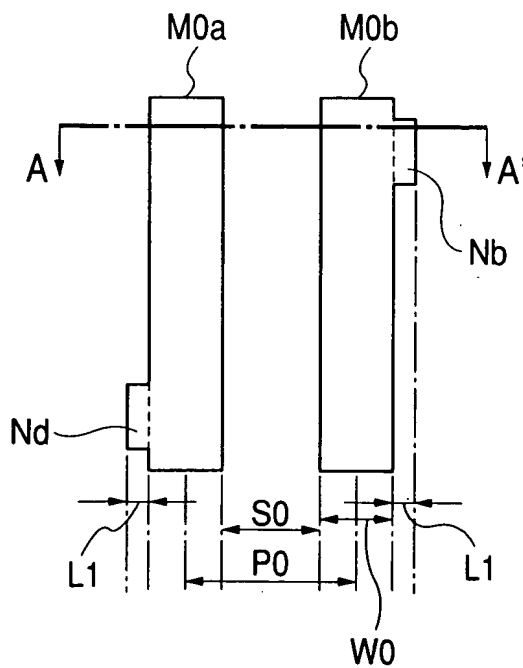


FIG. 4 is a schematic diagram illustrating a space error correction mechanism. The diagram shows a grid of components. At the top, there are two columns of components labeled M0a and M0b. Below M0a is TH0a, and below M0b is TH0b. A horizontal double-headed arrow labeled "SPACE ERROR" is positioned between M0a and M0b. Below TH0a and TH0b are two columns of components labeled M1a and M1b. A downward arrow labeled "MOVE" points from M0b to M1b. Below M1a and M1b is a row of components labeled M1c. A horizontal double-headed arrow labeled "S0" is positioned between M1a and M1b. A vertical double-headed arrow labeled "P0" is positioned between M1a and M1b. A vertical double-headed arrow labeled "S1" is positioned between M1b and M1c. A vertical double-headed arrow labeled "P1" is positioned between M1b and M1c. A horizontal double-headed arrow labeled "W0" is positioned between M1a and M1b. A horizontal double-headed arrow labeled "W1" is positioned between M1b and M1c. A coordinate system with X and Y axes is shown at the bottom right.

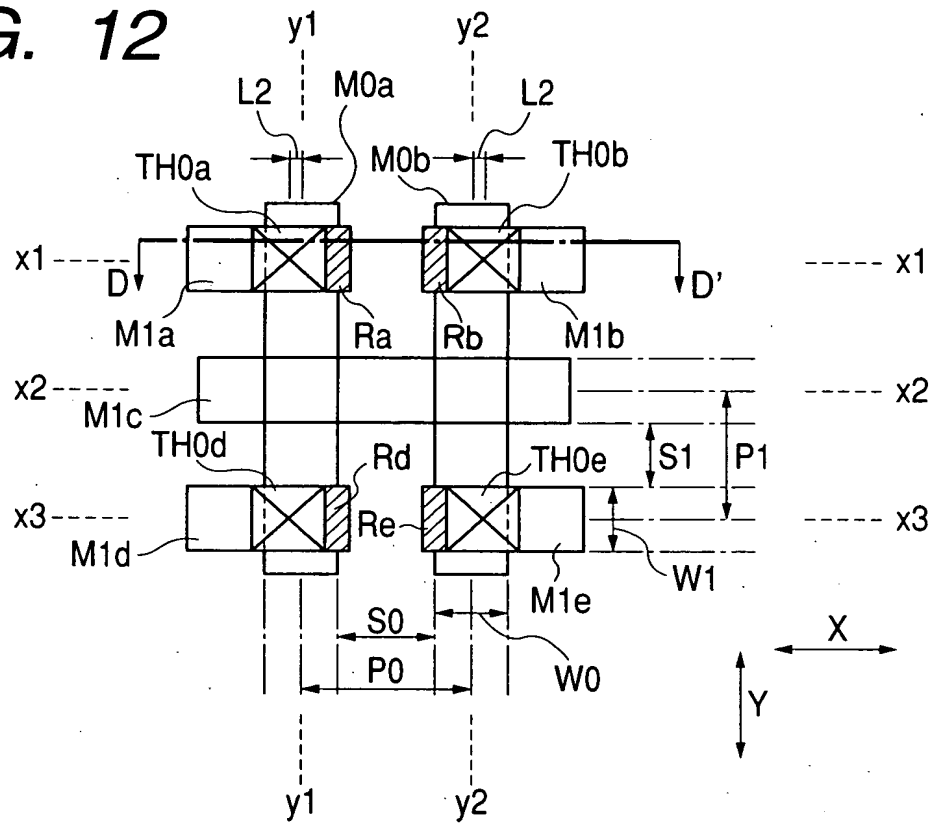
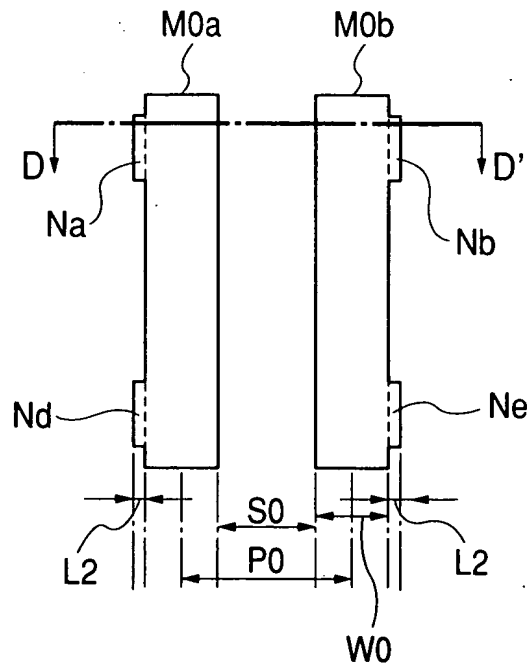
FIG. 12**FIG. 13**

FIG. 19

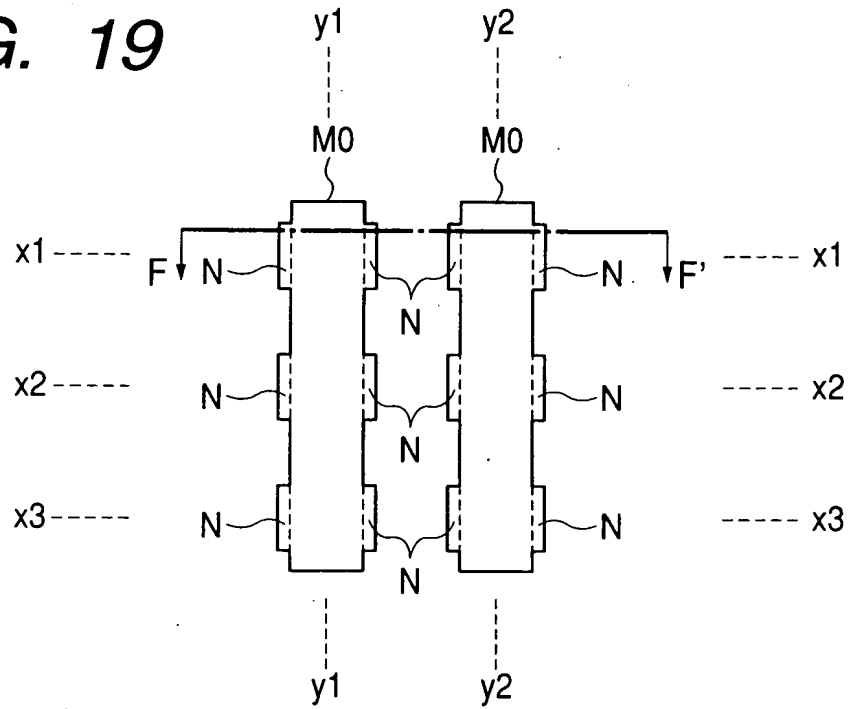


FIG. 20

